

METHOD AND APPARATUS FOR SERIAL DATA COMMUNICATION

BACKGROUND OF THE INVENTION

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1. Field of the Invention

The present invention relates to the technology of data transmission and reception between control units through a serial bus, and in particular to a method and an apparatus for serial data communication which is capable of accurate data
10 transmission and reception regardless of the change in circumstances by performing a two-way communication between two independent communication control units, processing a task independently by the one control unit when communication is disabled due to an abnormality of the other control unit.

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2. Description of the Prior Art

In the conventional serial data transmission technology, data transmission and reception is performed under the control of a master control unit. In other words, when the data transmission and reception is performed between two control units, a clock is controlled by a one master control unit. Accordingly, a
20 slave control unit transmits and receives data in accordance with the clock which is controlled by the master control unit.

Figure 1 and figure 3 illustrate normal data and transformation data which are on a bus in the conventional serial data transmission technology. Figure 1 illustrates an example of an IM bus protocol developed by ITT, figure 1a illustrates
25 normal data transmission, figure 1b illustrates data transformation caused by the

surrounding noise. Herein, the surrounding noise may be generated by an external symbol dictionary (ESD) or internal discharge of a color picture tube (CPT), and a signal for producing contrast 36hex data may be transformed into the signal for making contrast 16hex data due to the surrounding noise.

5 Figure 2 illustrates an example of a Philips 12C bus protocol which have been used widely in recent years. Figure 2a illustrates a normal data transmission, and figure 2b illustrates data transformation caused by the surrounding noise. Data change of a tuner may be considered as a countermeasure for such data transformation, but data of other control unit may be changed due to the data
10 change of the tuner. On the 12C bus, although it is possible to check whether the slave control unit received the data as a byte unit, an incorrect operation may be performed when the data is changed.

 Figure 3 illustrates a master control unit in a clock line occupation state or in a long communication pause state during the data transmission, at this time the
15 slave control unit can not perform independently.

 The conventional technology have the following problems. When transmitted data from the master control unit is damaged due to the surrounding noise, the slave control unit may process the data without recognizing the damage and may perform an inaccurate operation. When the master control unit stops the
20 data reception while receiving the data, in other words, when the master control unit holds the clock line due to its abnormality, the slave control unit can not perform any operation. When there is a need to get information from the master control unit or to transmit information on the slave control unit, it is difficult to get the information independently without the process operation of the master control
25 unit.

SUMMARY OF THE INVENTION

It is an object of the present invention to provide a method for serial data communication which is capable of accurate data transmission and reception regardless of the change in circumferences by performing a two-way communication between two control units having separate and independent communication control, adjusting a communication data quantity voluntarily by a definition of both control units, and performing communication between the control units which have different control voltages by complementing a circuit slightly.

To achieve the object, there is provided a method for serial data communication which transmits and receives two-way data between two control units. The method comprises a data transmission process which transmits data from a first control unit to a second control unit, at the same time checks a reception confirmation signal of the second control unit on each number of the data by transmitting a clock signal, and a data reception process which receives the data from the second control unit by the first control unit, at the same time checks a transmission confirmation signal of the second control unit on the each number of the data by transmitting the clock signal.

When the first control unit transmits serial data to the second control unit, the data transmission process includes the steps of (a) confirming whether data was received, starting data transmission with a data transmission start signal and receiving the reception confirmation signal from the second control unit, (b) recognizing the data reception of the second control unit and reversing the transmission start signal in order to transmit the next data, (c) transmitting the

transmission permission signal which informs the second control unit to transmit the next data, and (d) feedback-receiving the transmission permission signal. The data is transmitted to the second control unit by performing the steps repeatedly at certain intervals.

5 The data reception process includes steps of (a) receiving the first data from the second control unit and performing feedback for informing the data reception, (b) receiving a next data transmission preparation signal from the second control unit, (c) transmitting the data transmission permission signal to the second control unit, and (d) transmitting the next data on the second control unit
10 after feedback-receiving the data transmission permission signal. The data is received from the second control unit by performing the steps repeatedly at certain times.

BRIEF DESCRIPTION OF THE DRAWINGS

15 Figure 1a is a waveform diagram illustrating an example of a normal data on an IM bus.

 Figure 1b is a waveform diagram illustrating data transformation on the IM bus due to surrounding noise.

20 Figure 2a is a waveform diagram illustrating a normal data on a 12C bus.

 Figure 2b is a waveform diagram illustrating the data transformation on the 12C bus due to the surrounding noise.

 Figure 3 is a waveform diagram illustrating long communication trial state or communication pause state on the 12C bus.

25 Figure 4 is a block diagram illustrating preferred embodiment of serial data

control units of the present invention.

Figure 5 is a waveform diagram illustrating data transmission mode of the present invention.

Figure 6 is a waveform diagram illustrating data reception mode of the present invention.

Figure 7 is a circuit diagram of control voltage matching for bus lines of the present invention.

Figure 8a-8c are flow charts illustrating a method for serial data transmission control of the present invention.

Figure 9 is a flow chart illustrating the method for serial data reception control of the present invention.

DETAILED DESCRIPTION OF THE PRESENT INVENTION

Figure 4 is a schematic block diagram illustrating a serial bus line connection applied to a method for a serial data communication of the present invention. It comprises a first control unit 1 and a second control unit 2, each control unit can operate as a master control unit or a slave control unit according to need, when data is transmitted and received, the both control units transmit and receive various recognition signals and confirmation signals before and after of data unit through a clock signal as a medium.

The operation of the present invention will be described in detail with reference to the accompanying figure5 and figure9.

Figure 4 illustrates four bus lines installed between the first control unit 1 and the second control unit 2, TXD is data transmitted to the second control unit 2,

RXD is data received on the first control unit 1, TXC is a clock signal transmitted to the second control unit 2, RXC is a clock signal received on the first control unit 1.

Herein, both control units 1 and 2 are not fixed as the master control unit or the slave control unit, but operate as the master control unit or the slave control unit according to situations described below.

Figure 5 illustrates a signal waveform on each bus line for indicating a protocol applied to the present invention at a data transmission mode. On the basis of the first control unit 1, when a transmission clock signal TXC and the reception clock signal RXC are "high", the transmission data TXD starts the data transmission at the time of "low" conversion of the transmission clock signal TXC.

After the first control unit 1 outputs a transmission start signal, t1 section receives a data reception confirmation signal from the second control unit 2, length(time) of the t1 section is set in consideration of a data processing cycle of the second control unit 2. It is advisable for the t1 section to be set at 20-70ms according to experimental results, this is the maximum wait time considering an inputted signal cycle of a television set which is 100-120ms at consecutive operations of a remote-controller.

After all, the t1 section is for informing a first data transmission-reception and is data effective section.

t2 section converts the clock signal TXC into "high" in order to transmit the next data after the master control unit, namely, the first control unit 1 recognizes the data reception of the second control unit 2, and the t2 section (time) is set in consideration of a feedback time of the second control unit 2 after receiving and processing the data of the second communication unit 2.

t3 section is for transmitting a signal which permits the next data

transmission to the first control unit 1 on the slave control unit (the second control unit 2), the t3 section is set in consideration of the feedback time of the first control unit 1.

t4 section is for feedback-receiving the signal which permits the next data transmission from the second control unit 2 on the first control unit 1, at this time the first control unit 1 converts the next data and transmits the converted data.

t5 section is for receiving the data reception confirmation signal from the second control unit 2 as well as the t1 section, the t5 section is set in consideration of a feedback receiving time from the second control unit 2 because data transmission mode has started already at this point.

t6 section performs the same function with the t2 section, t7 section performs the same function with the t4 section, and t9 section performs the same function with the t5 section. Numbers of transmitted data through the above process are changed freely by using the protocol between the first control unit 1 and the second control unit 2.

Figure 6 is a waveform diagram at the data reception mode illustrating signal waveforms on each bus line in order to indicate the protocol applied to the present invention. On the basis of the first control unit 1, at the data reception mode, when the reception clock signal RXC and the transmission clock signal TXC are "high", the data reception is started at the time of "low" conversion of the reception clock signal RXC. On the contrary with the above reception mode, a feedback signal is transmitted through the clock signal TXC in accordance with the clock signal RXC received from the second control unit 2.

The t1 section receives a first data from the second control unit 2 and feedback-performs the first data reception through the transmission clock signal

TXC, and the t2 section converts the clock signal RXC into "high" on the second control unit 2 in order to indicate preparation of the next data transmission.

The t3 section is for transmitting a transmission permission signal to the second control unit 2 through the transmission clock signal TXC, the t4 section is
5 for transmitting the next data after receiving the transmission permission signal on the second control unit 2.

The t5 section is for receiving and feedback-performing likewise the t1 section, length of the t5 section is set in consideration of the feedback receiving time of the second control unit 2 because the data reception mode has started
10 already at this point.

The t6 section performs the same function with the t2 section, the t7 section performs the same function with the t3 section, the t8 section performs the same function with the t4 section, and the t9 section performs the same function with the t5 section. Numbers of the received data through the above process are
15 changed freely by using the protocol between the first control unit 1 and the second control unit 2.

Figure 7 illustrates the both control units and added pullup-pulldown circuits for communicating smoothly when a control voltage level of the both control units is different. For example, when the first control unit 1 uses 5V, recognizes 3-5V as "high" and 0-0.7V as "low", the second control unit 2 uses 3.3V,
20 recognizes 2-3V as "high" and 0-0.7V as "low", in this case the first control unit 1 controls the voltage of the TXD bus line and the TXC bus line. In other words, when the pullup resistance R1 and R2 are connected between the power terminal 5V and the TXD, TXC bus lines of the first control unit 1, the first control unit 1
25 keeps the voltage balance through a control voltage matching unit 4.

Levels of the reception data and the clock signal RXD, RXC of the second control unit 2 are same with levels of the reception data and the clock signal RXD, RXC of the first control unit 1 by the control voltage matching unit 4 including resistance R5, R6 serial-connected to the TXD, TXC bus lines which connects the
5 other connection point of the resistance R5, R6 to a ground terminal through resistance R7, R8.

Meanwhile, figure 8a-8c are signal flow charts of the data transmission mode of the present invention, a method for the serial data transmission of the present invention will be described with reference to the accompanying figures 8a-
10 8c. First, when the transmission mode is started as depicted in figure 8a, the first control unit 1 confirms whether the data is transmitted from the second control unit 2, when the transmitted data is confirmed, in other words, the reception clock signal RXC is "low", the first control unit 1 reception-processes the transmitted data, and transmits the data (SA1, SA2).

15 When the data transmission fails, re-transmission is performed, repetition count is set at three in the present example (SA3).

Number of the data TXD unit is set at 3byte, namely, 24bit are set as the number of the data unit in the present invention (SA4).

When the preparation process as above is completed, the clock signal
20 TXC is set "low", and the transmission data TXD is transmitted to the second control unit 2 (SA5).

After transmitting the transmission data TXD, it is confirmed whether a feedback signal from the second control unit 2 is inputted within a certain time, when the feedback signal is inputted, normal data transmission is confirmed, when
25 the feedback signal is not inputted, transmission error is confirmed (SA6, SA7).

When the transmission error occurrence is confirmed, the transmission operation is instantly stopped, it is confirmed whether the repetition count is all counted, when the count is all counted, the transmission process returns to the first, when the repetition count is not all counted, after waiting a certain period
5 (1ms) returns to said (d) process, and re-transmission is performed (SA8, SA9).

But, when the transmission error does not occur, after converting the transmission clock signal TXC into "high" in order to transmit the next transmission data TXD, a feedback signal is checked in order to confirm reception standby of the other communication unit (SA10, SA11).

10 As a result of the confirmation, when the feedback signal is inputted, normal data transmission is confirmed, when the feedback signal is not inputted, the transmission error occurrence is confirmed and re-transmission is performed by proceeding the above SA8, SA9 process (SA12).

When the normal data transmission is confirmed, it is confirmed whether
15 the data is all transmitted, when all the data transmission is confirmed, the transmission process returns to the first, when there is remaining data to be transmitted, after preparing the next data, returning to the SA5 process, and the above process is repeated (SA13, SA14).

Figure 8b illustrates a routine for checking a feedback signal input from the
20 second control unit 2 after transmitting the transmission data TXD when the transmission clock signal TXD is "low". A time for receiving a feedback signal from the second control unit 2 after the first data output and a time for receiving the feedback signal from the second control unit 2 after consecutive outputs are set. In the present example, maximum 50ms at first, after that a maximum 100μs are set
25 (SB1-SB4).

When the reception clock signal RXC as the feedback signal is not converted into "low" in the given time, an error flag is set for error processing, when the RXC is converted into "low" in the given time, the error flag is cleared (SB5-SB8).

5 Figure 8c illustrates a check routine which receives the feedback signal from the second control unit 2 for preparing the next data after outputting the transmission clock signal TXC when the TXC is "high". First, a time for receiving the feedback signal from the second control unit 2 is set. In the present example, maximum 100 μ s for receiving the feedback signals are set (SC1, SC2).

10 When the reception clock signal RXC as the feedback signal is not converted into "high" in the given time, an error flag is set for error processing, when the reception clock signal RXC is converted into "high" in the given time, the error flag is cleared (SC5-SC6).

15 Meanwhile, figures 9 is a signal flow chart of the data reception mode of the present invention, the method for serial data reception of the present invention is described with reference to the accompanying figure 9. Herein, an example of 1 byte (8 bit) data reception processing is described below.

20 When the reception process is started, number (eight) of reception data is set, the reception clock signal RXC is checked whether it is "low", when "low" is not detected in the given time, the error processing is performed (SD1-SD3, SD9). But, when "low" is detected, the data reception is performed because the data is effective, the check operation is performed repeatedly in order to increase reliability of data reception (SD4). After the data reception, a feedback of the transmission clock signal TXC is performed, the clock signal TXC becomes the
25 reception clock signal RXC on the second control unit 2.

Afterwards, preparation completion for receiving the next data is confirmed, when error occurs on the second control unit 2 during the transmission and the reception clock signal RXC is not converted into "high" independently, the error flag is set after given maximum wait time (SD5).

5 But, when the data is received normally, a signal for requesting for the next data transmission is transmitted to the second control unit 2 by converting the clock signal TXC into "high" (SC6, SD7).

When the data is completely inputted as the set data number through the above reception process, the reception process is finished, when the data is completely not inputted, after returning to the first process (SD1), the above process is repeated.

In the data reception process, when error occurs, not receiving the data again, but the transmission data TXD and the transmission clock signal TXC are converted into "high" in order to receive the data on the check routine, accordingly the process returns in order to receive the data on the next check routine.

As described above, the present invention is capable of accurate data transmission and reception by transmitting the data as a certain unit and feedback-receiving the confirmation signal during the transmission, operating a first control unit voluntarily when error occurs in a second control unit because both control units are not fixed as a master control unit or a slave control unit, but either control unit performs the master control unit function and two-way communication by transmitting-receiving the confirmation signal in accordance with the situation, and adjusting communication data quantity or communication rate by newly setting the protocol between both control units in accordance with the communication circumstances. Accordingly, the present invention can provide a method for serial

data communication which can perform the optimum communication in any communication circumstances without a circuit design alteration.

In addition, it is possible to provide normal communication between communication units having different control voltage by adding a control voltage
5 matching circuit between both control units having different bus control voltage.

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